

### Course Outcomes

CS405.1	Students will be able to summarize different parallel computer models
CS405.2	Students will be able to analyze the advanced processor technologies
CS405.3	Students will be able to Interpret memory hierarchy
CS405.4	Students will be able to compare different multiprocessor system interconnecting mechanisms.
CS405.5	Students will be able to Interpret the mechanisms for enforcing cache coherence
CS405.6	Students will be able to analyze different message passing mechanisms
CS405.7	Students will be able to analyze different pipe lining techniques
CS405.8	Students will be able to appraise concepts of multithreaded and data flow architectures