





	RACE CONDITION							
 Consider two cooperating processes, sharing variables A and B and having the following set of instructions in each of them: 								
	Process 1	Process 2	Concurrent access					
	A=1	B=2	Does not matter					
	A=B+1 $B=B+1$	B=B*2	Important!					
• Suppose our intention is to get A as 3 and B as 6 after the execution of both the processes. The interleaving of these instructions should be done in order to avoid race condition.								
• If the order of execution is like:								
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• Calls to either acquire() or release() must be performed atomically. The main disadvantage of the implementation given here is that it requires busy waiting. • While a process is in its critical section, any other process that tries to enter its critical section must loop continuously in the call to acquire(). • In fact, this type of mutex lock is also called a **spinlock** because the process spins while waiting for the lock to become available. • This continual looping is clearly a problem in а real multiprogramming system, where a single CPU is shared among many processes. Prepared By Mr. EBIN PM, AP EDULINE 21





MONITORS					
The incorrect use of semaphores can result in timing errors.					
Suppose that a process interchanges the order in which the wait() and signal() operations on the semaphore mutex are executed, resulting in the following execution:					
signal(mutex); critical section wait(mutex);	In this situation, several processes may be executing in their critical sections simultaneously, violating the mutual-exclusion requirement.				
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The structure of a writer process		
<pre>do { wait (wrt); /* writing is performed */ signal(wrt); } while (true); initially , wrt=1</pre>		
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